

IN THE CLAIMS:

Please amend claims 1, 7, 8, 11, 12, 15, 19, 20, 23, 26, 27, and 30, and please add new claims 31-35, as set forth below.

1 1. (Currently Amended) A method comprising:
2 requesting a first deferred procedure call for a first interrupt event associated with a
3 source;
4 requesting at least one other deferred procedure call for a second interrupt event
5 associated with the source;
6 assigning the first deferred procedure call and the at least one other deferred procedure
7 call to a resource;
8 processing the first interrupt event with the first deferred procedure call; and
9 processing the second interrupt event with the at least one other deferred procedure call.

1 2. (Original) The method of claim 1, further comprising:
2 assigning the first deferred procedure call and the at least one other deferred procedure
3 call to a resource comprising a processor exhibiting a single thread of execution;
4 and
5 executing the first deferred procedure call and the at least one other deferred procedure
6 call on the single thread.

1 3. (Original) The method of claim 1, further comprising:
2 assigning the first deferred procedure call and the at least one other deferred procedure
3 call to a resource comprising a processor exhibiting a plurality of threads; and
4 executing the first deferred procedure call on one thread of the plurality of threads while
5 executing the at least one other deferred procedure call on another thread of the
6 plurality of threads.

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1 4. (Original) The method of claim 1, further comprising:
2 assigning the first deferred procedure call to a resource comprising a first thread of a
3 processor;
4 assigning the at least one other deferred procedure call to a resource comprising a second
5 thread of the processor; and
6 executing the first deferred procedure call on the first thread while executing the at least
7 one other deferred procedure call on the second thread.

1 5. (Original) The method of claim 1, further comprising:
2 assigning the first deferred procedure call and the at least one other deferred procedure
3 call to a resource comprising a multi-processor system; and
4 executing the first deferred procedure call on one processor of the multi-processor system
5 while executing the at least one other deferred procedure call on another processor
6 of the multi-processor system.

1 6. (Original) The method of claim 1, further comprising:
2 assigning the first deferred procedure call to a resource comprising a first processor;
3 assigning the at least one other deferred procedure call to a resource comprising a second
4 processor; and
5 executing the first deferred procedure call on the first processor while executing the at
6 least one other deferred procedure call on the second processor.

1 7. (Currently Amended) The method of claim 1, further comprising
/ 2 processing another interrupt event with ~~one of~~ the first deferred procedure call and or the
a 3 at least one other deferred procedure call.

1 8. (Currently Amended) A method comprising:
2 requesting a first deferred procedure call for a first interrupt event associated with a
3 source;
4 requesting at least one other deferred procedure call for a second interrupt event
5 associated with the source; and
6 processing the first interrupt event with the first deferred procedure call while processing
7 the second interrupt event with the at least one other deferred procedure call.

1 9. (Original) The method of claim 8, further comprising:
2 executing the first deferred procedure call on a first thread of a processor; and
3 executing the at least one other deferred procedure call on a second thread of the
4 processor.

1 10. (Original) The method of claim 8, further comprising:
2 executing the first deferred procedure call on a first processor; and
3 executing the at least one other deferred procedure call on a second processor.

1 11. (Currently Amended) The method of claim 8, further comprising
2 processing another interrupt event with ~~one of~~ the first deferred procedure call ~~and~~ or the
3 at least one other deferred procedure call.

1 12. (Currently Amended) A driver comprising:
2 an interrupt handler to identify interrupt events associated with a source; and
3 at least two deferred procedure calls, each of the at least two deferred procedure calls to
4 process at least one of the interrupt events.

1 13. (Original) The driver of claim 12, the interrupt handler to assign the at
2 least two deferred procedure calls to a resource for execution.

1 14. (Original) The driver of claim 12, the interrupt handler to assign one of
2 the at least two deferred procedure calls to a first resource for execution and another of
3 the at least two deferred procedure calls to a second resource for execution.

1 15. (Currently Amended) A computer system comprising:
2 a driver stored in a memory of the computer system, the driver including
3 an interrupt handler to identify interrupt events associated with a source; and
4 at least two deferred procedure calls, each of the at least two deferred procedure
5 calls to process at least one of the interrupt events.

6 and
7 a processor to execute the at least two deferred procedure calls.

1 16. (Original) The computer system of claim 15, the interrupt handler to
2 assign the at least two deferred procedure calls to a single thread exhibited by the
3 processor for execution.

1 17. (Original) The computer system of claim 15, the interrupt handler to
2 assign a first of the at least two deferred procedure calls to one thread of the processor
3 and another of the at least two deferred procedure calls to a second thread of the
4 processor for execution.

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1 18. (Original) The computer system of claim 15, the interrupt handler to
2 assign one of the at least two deferred procedure calls to the processor and another of the
3 at least two deferred procedure calls to a second processor for execution.

1 19. (Currently Amended) The computer system of claim 15, ~~further~~
2 ~~comprising at least one peripheral device, the interrupt events associated with the at least~~
3 ~~one wherein the source comprises a peripheral device coupled with the computer system.~~

1 20. (Currently Amended) An article of manufacture comprising:
2 a machine accessible medium, the machine accessible medium providing instructions
3 that, when executed by a machine, cause the machine to:

4 request a first deferred procedure call for a first interrupt event associated with a
5 source;

6 request at least one other deferred procedure call for a second interrupt event
7 associated with the source;

8 assign the first deferred procedure call and the at least one other deferred
9 procedure call to a resource;

10 process the first interrupt event with the first deferred procedure call; and

11 process the second interrupt event with the at least one other deferred procedure
12 call.

1 21. (Original) The article of claim 20, wherein the instructions, when
2 executed, further cause the machine to:
3 assign the first deferred procedure call and the at least one other deferred procedure
4 call to a resource comprising a processor exhibiting a single thread of execution; and
5 execute the first deferred procedure call and the at least one other deferred procedure call
6 on the single thread.

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1 22. (Original) The article of claim 20, wherein the instructions, when
2 executed, further cause the machine to:
3 assign the first deferred procedure call and the at least one other deferred procedure
4 call to a resource comprising a processor exhibiting a plurality of threads; and
5 execute the first deferred procedure call on one thread of the plurality of threads while
6 executing the at least one other deferred procedure call on another thread of the
7 plurality of threads.

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1 23. (Currently Amended) The article of claim 20, wherein the instructions,
2 when executed, further cause the machine to:
3 assign the first deferred procedure call to a resource comprising a first thread of a
4 processor;
5 assign the at least one other deferred procedure call to a resource comprising a second
6 thread of the processor; and
7 execute the first deferred procedure call on the first thread while executing the at least
8 one other deferred procedure call on the second thread ~~thread~~.

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1 24. (Original) The article of claim 20, wherein the instructions, when
2 executed, further cause the machine to:
3 assign the first deferred procedure call and the at least one other deferred procedure call
4 to a resource comprising a multi-processor system; and
5 execute the first deferred procedure call on one processor of the multi-processor system
6 while executing the at least one other deferred procedure call on another processor
7 of the multi-processor system.

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1 25. (Original) The article of claim 20, wherein the instructions, when
2 executed, further cause the machine to:
3 assign the first deferred procedure call to a resource comprising a first processor;
4 assign the at least one other deferred procedure call to a resource comprising a second
5 processor; and
6 execute the first deferred procedure call on the first processor while executing the at least
7 one other deferred procedure call on the second processor.

a 1 26. (Currently Amended) The article of claim 20, wherein the instructions,
2 when executed, further cause the machine to process another interrupt event with ~~one of~~
3 the first deferred procedure call ~~and~~ or the at least one other deferred procedure call.

1 27. (Currently Amended) An article of manufacture comprising:
2 a machine accessible medium, the machine accessible medium providing instructions
3 that, when executed by a machine, cause the machine to:
4 request a first deferred procedure call for a first interrupt event associated with a
5 source;
6 request at least one other deferred procedure call for a second interrupt event
7 associated with the source; and
8 process the first interrupt event with the first deferred procedure call while
9 processing the second interrupt event with the at least one other deferred
10 procedure call.

1 28. (Original) The article of claim 27, wherein the instructions, when
2 executed, further cause the machine to:
3 execute the first deferred procedure call on a first thread of a processor; and
4 execute the at least one other deferred procedure call on a second thread of the processor.

1 29. (Original) The article of claim 27, wherein the instructions, when
2 executed, further cause the machine to:
3 execute the first deferred procedure call on a first processor; and
4 execute the at least one other deferred procedure call on a second processor.

a 1 30. (Currently Amended) The article of claim 27, wherein the instructions,
2 when executed, further cause the machine to process another interrupt event with ~~one of~~
3 the first deferred procedure call ~~and~~ or the at least one other deferred procedure call.

1 31. (New) The method of claim 1, wherein the source comprises a peripheral
2 device of a computer system.

1 32. (New) The method of claim 8, wherein the source comprises a peripheral
2 device of a computer system.

1 33. (New) The driver of claim 12, wherein the source comprises a peripheral
2 device of a computer system.

1 34. (New) The article of manufacture of claim 20, wherein the source
2 comprises a peripheral device of a computer system.

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1 35. (New) The article of manufacture of claim 27, wherein the source
2 comprises a peripheral device of a computer system.
